

FIG. 1

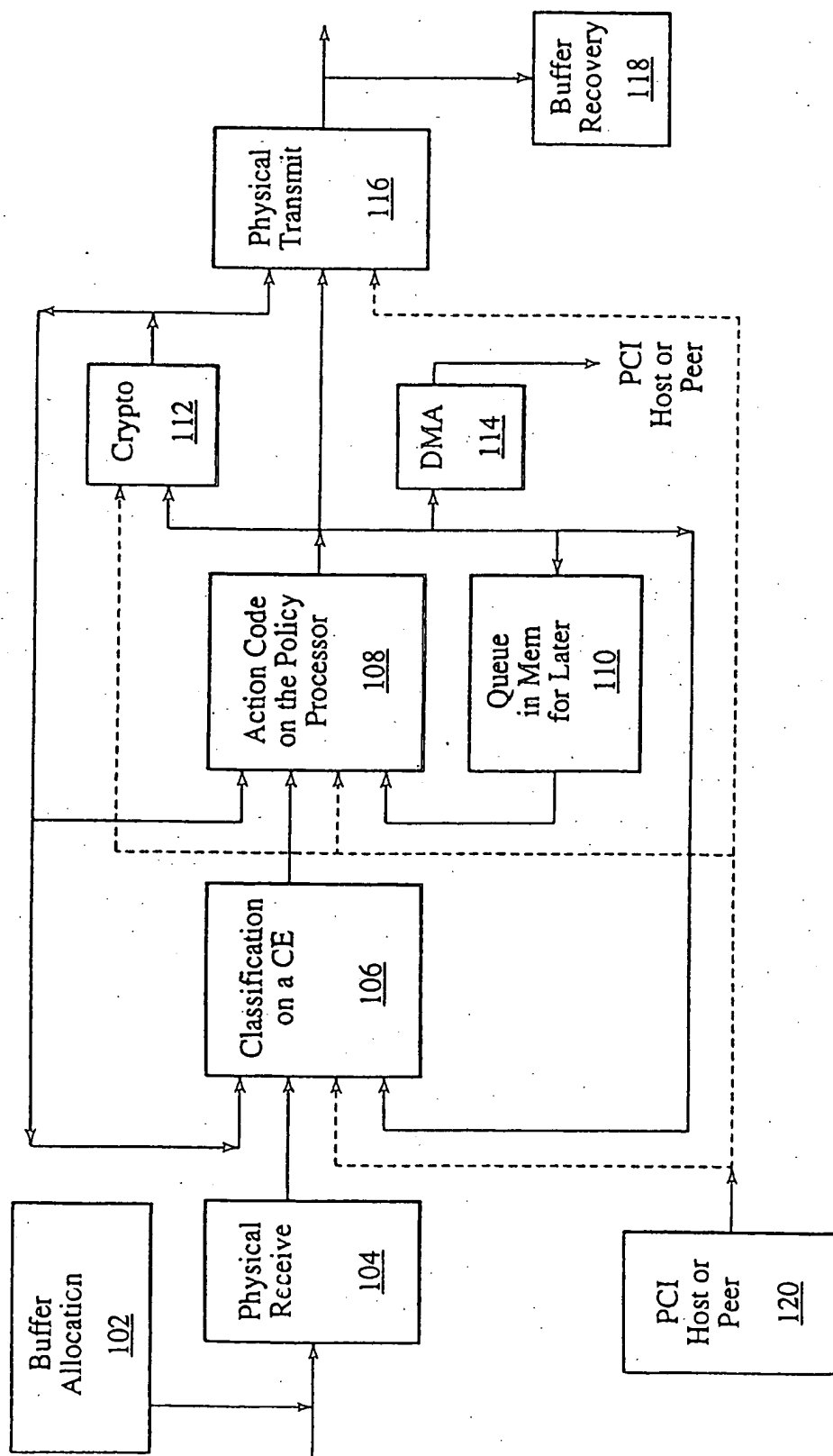
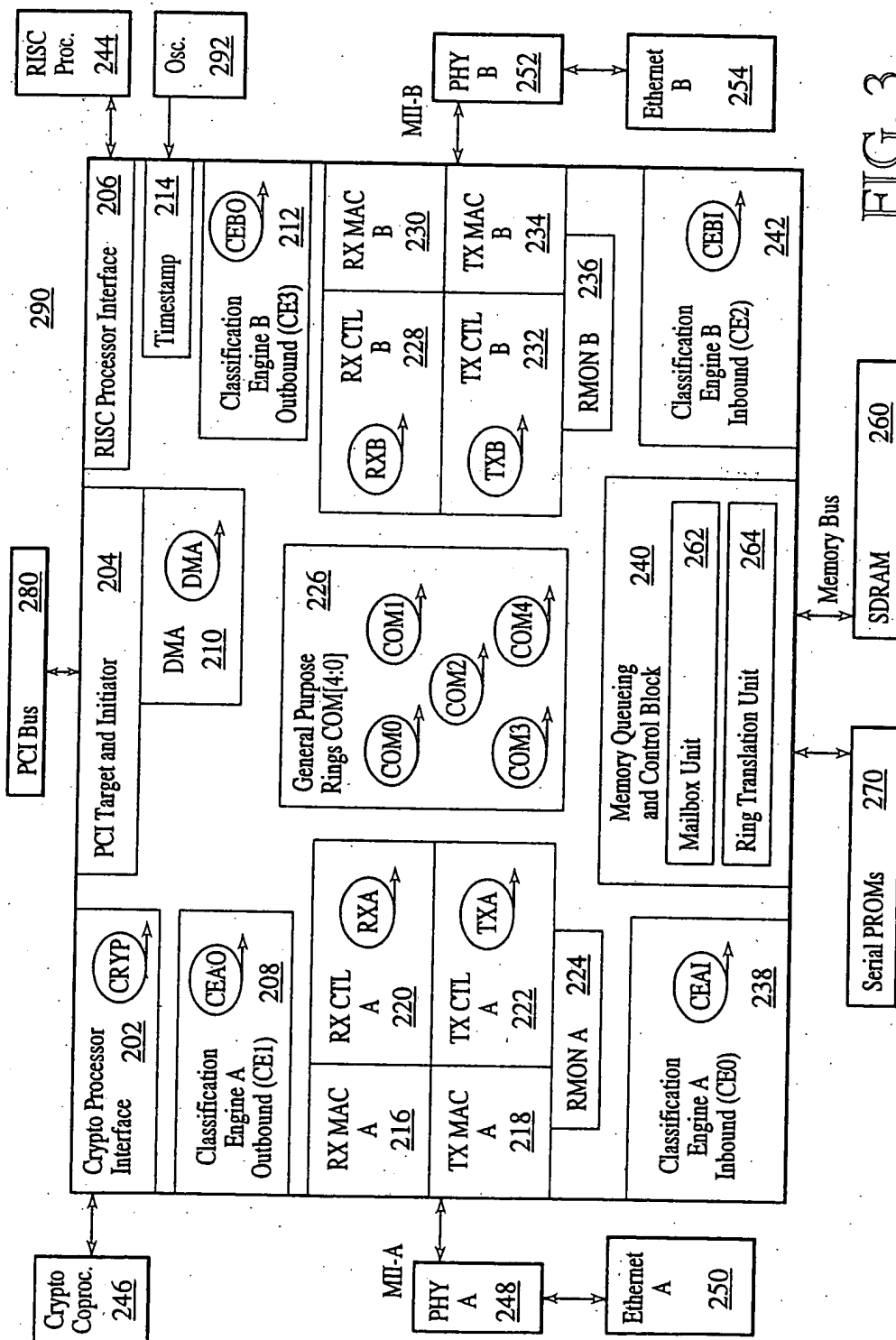


FIG. 2



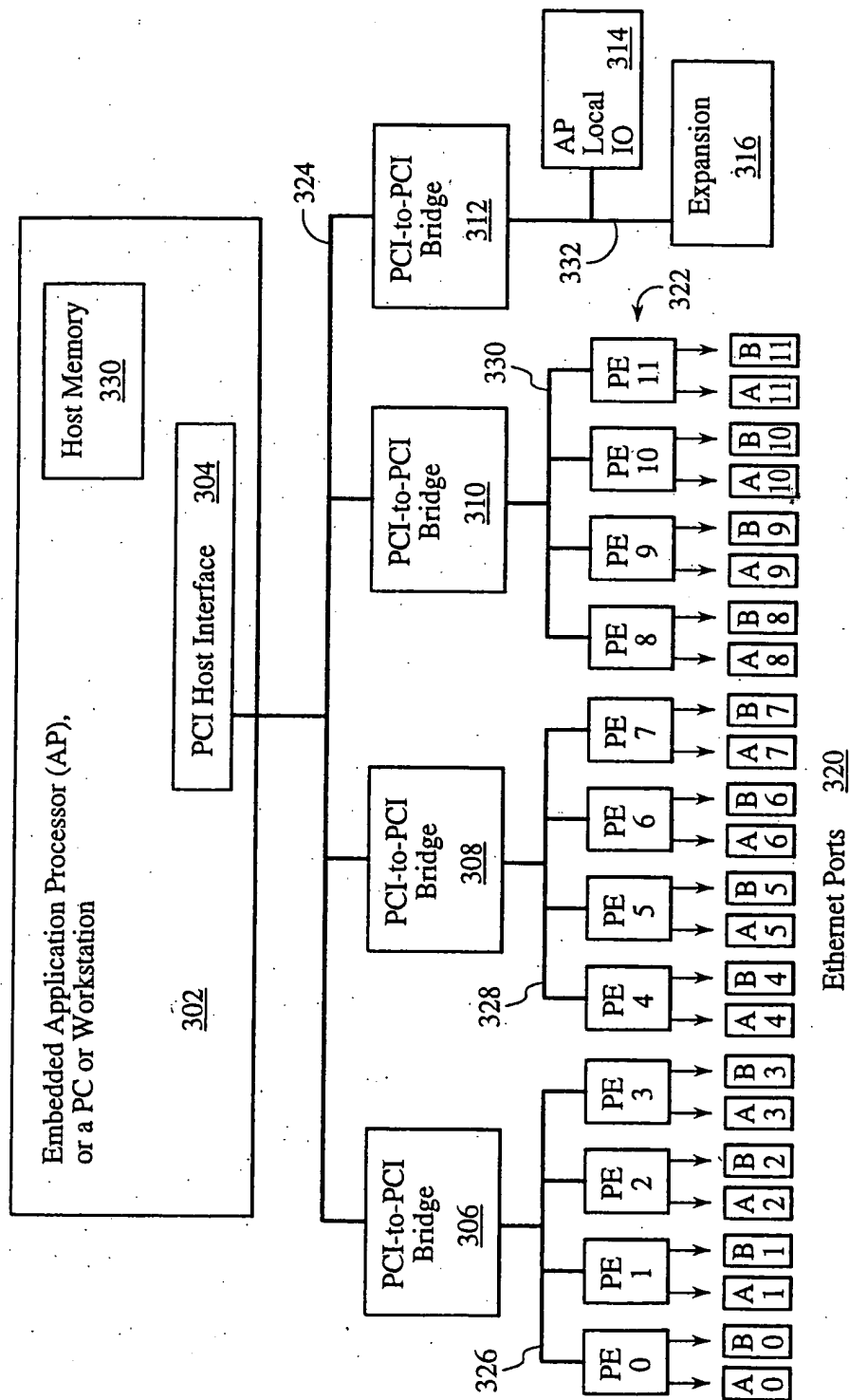


FIG. 4

Ring Base Register 400

+ 0x0000	RX_A Ring	402
+ 0x1000	RX_B Ring	404
+ 0x2000	TX_A Ring	406
+ 0x3000	TX_B Ring	408
+ 0x4000	Reclassify_A_Inbound Ring	410
+ 0x5000	Reclassify_A_Outbound Ring	412
+ 0x6000	Reclassify_B_Inbound Ring	414
+ 0x7000	Reclassify_B_Outbound Ring	416
+ 0x8000	DMA Ring	418
+ 0x9000	Crypto Ring	420
+ 0xA000	COM0 Ring	422
+ 0xB000	COM1 Ring	424
+ 0xC000	COM2 Ring	426
+ 0xD000	COM3 Ring	428
+ 0xE000	COM4 Ring	430

440

THRESHOLD REPORTED

- <256 valid between MPROD & MFILL
- <256 valid between MPROD & MFILL
- <256 empty between MTPROD & MTRECOV
- <256 empty between MTPROD & MTRECOV
- <256 empty between RPROD & RPCONS
- <256 empty between RPROD & RPCONS
- <256 empty between RPROD & RPCONS
- <256 empty between RPROD & RPCONS
- <256 empty between DMA_PROD & DMA_RECOV
- <256 empty between CRYPT_PROD & CRYPT_CONS

The 5 General Purpose Rings have Prog.
<256-empty/<256-full Threshold as set in
the RBASE Register.

FIG. 5

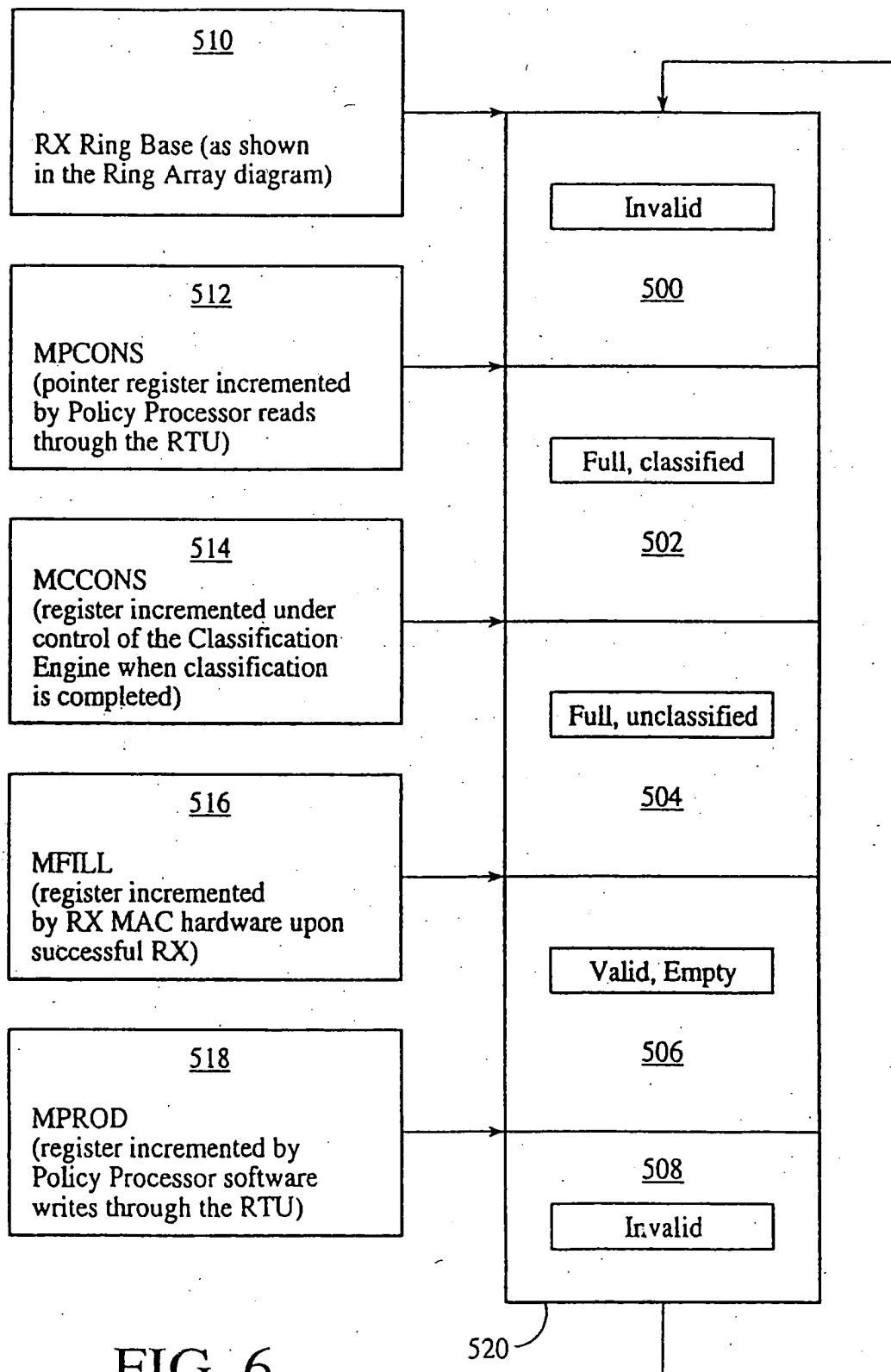


FIG. 6

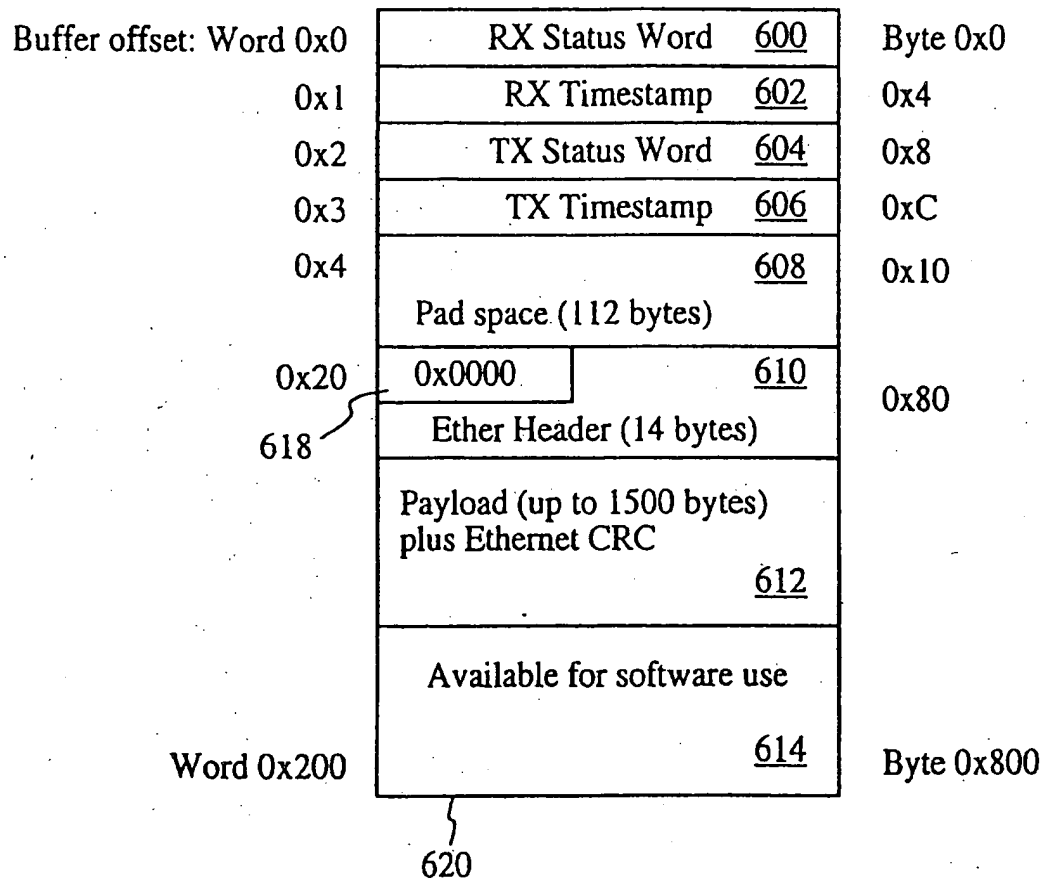


FIG. 7

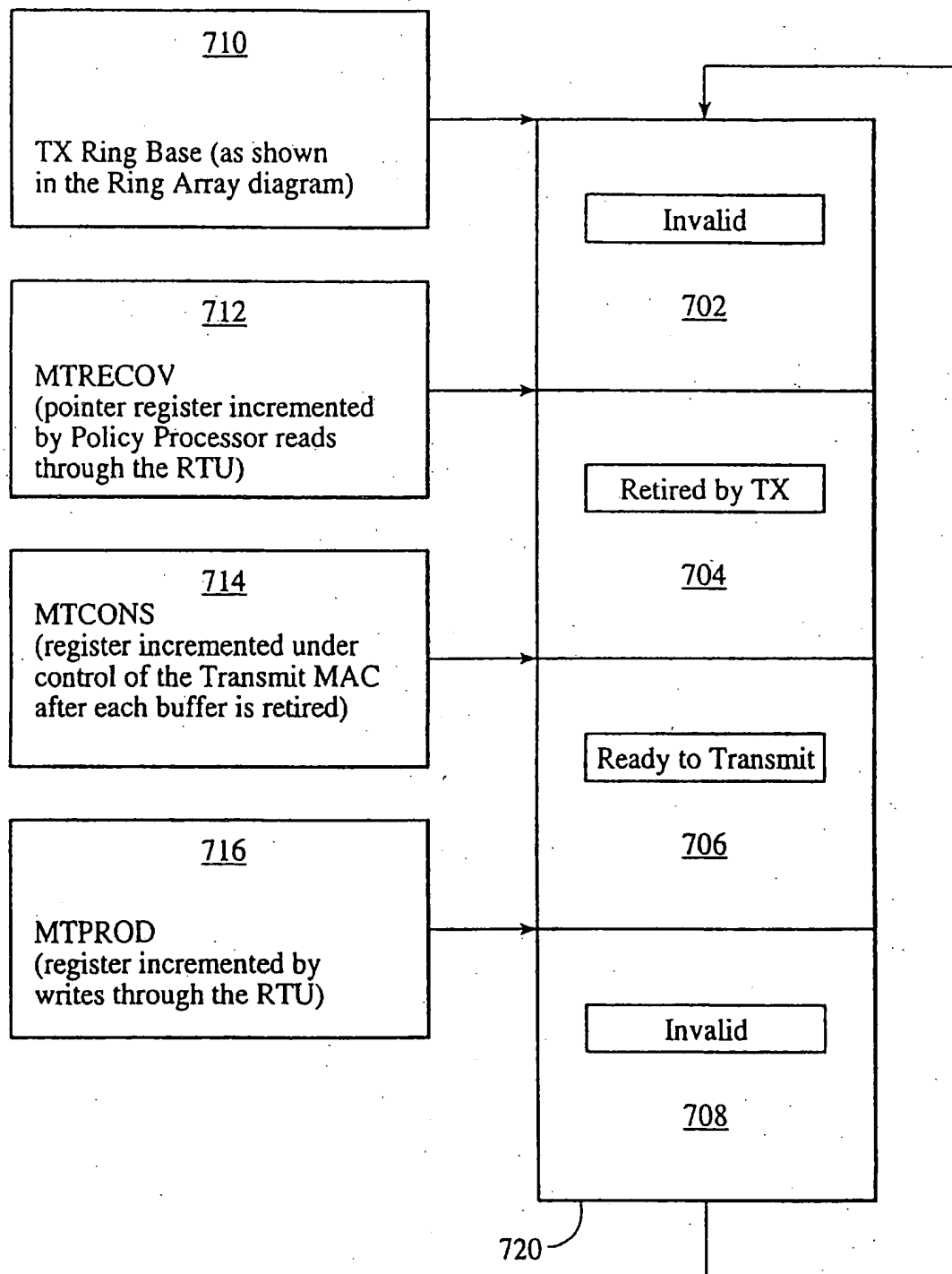


FIG. 8

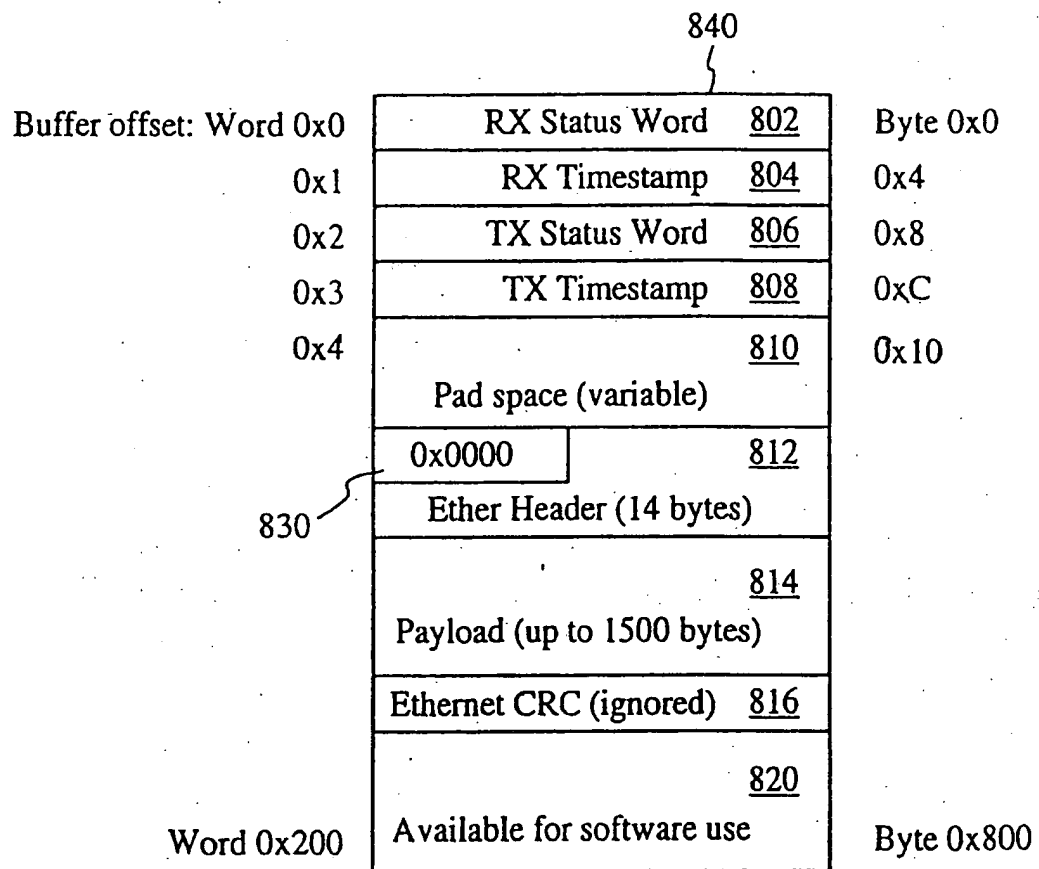


FIG. 9

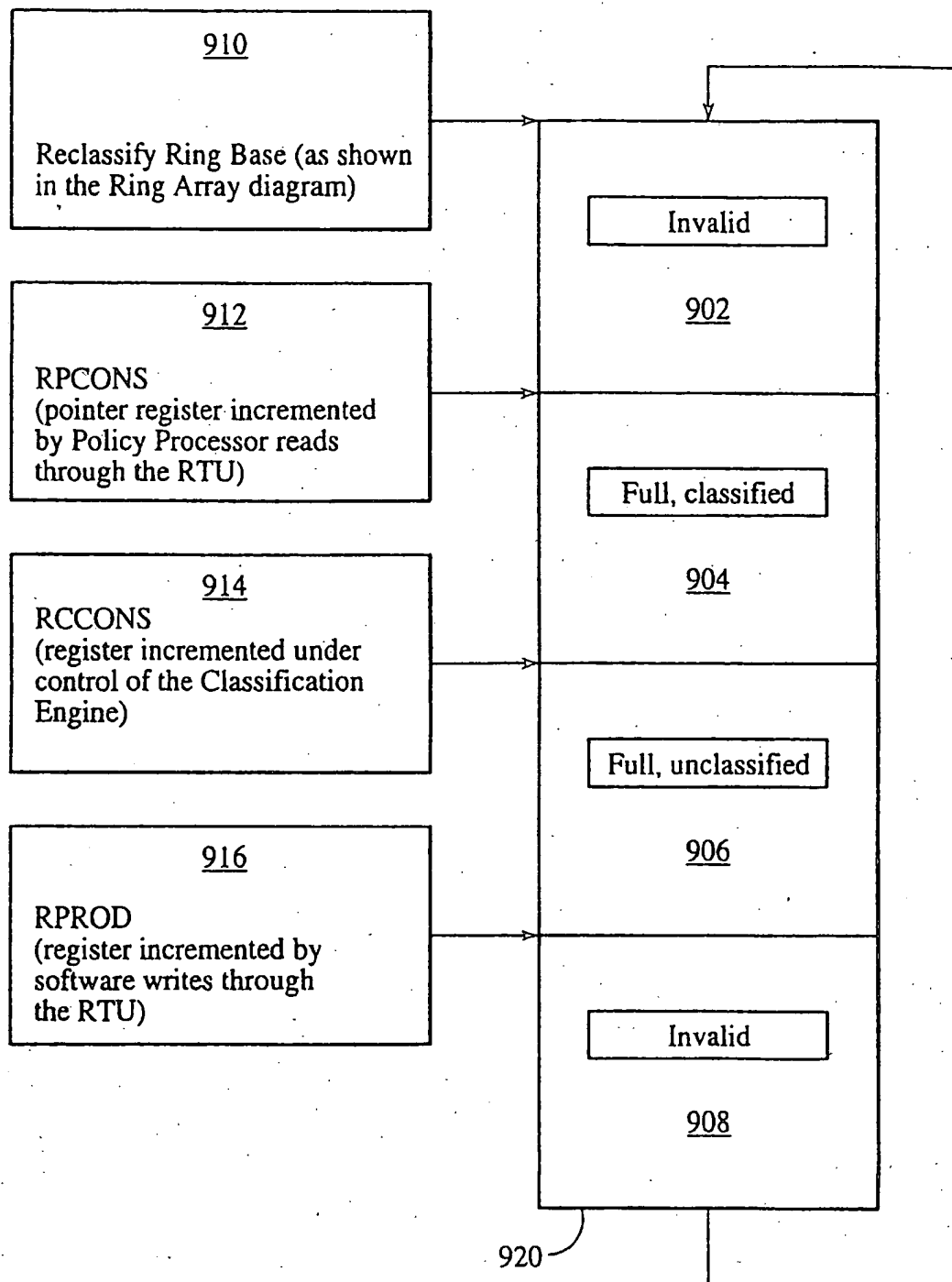


FIG. 10

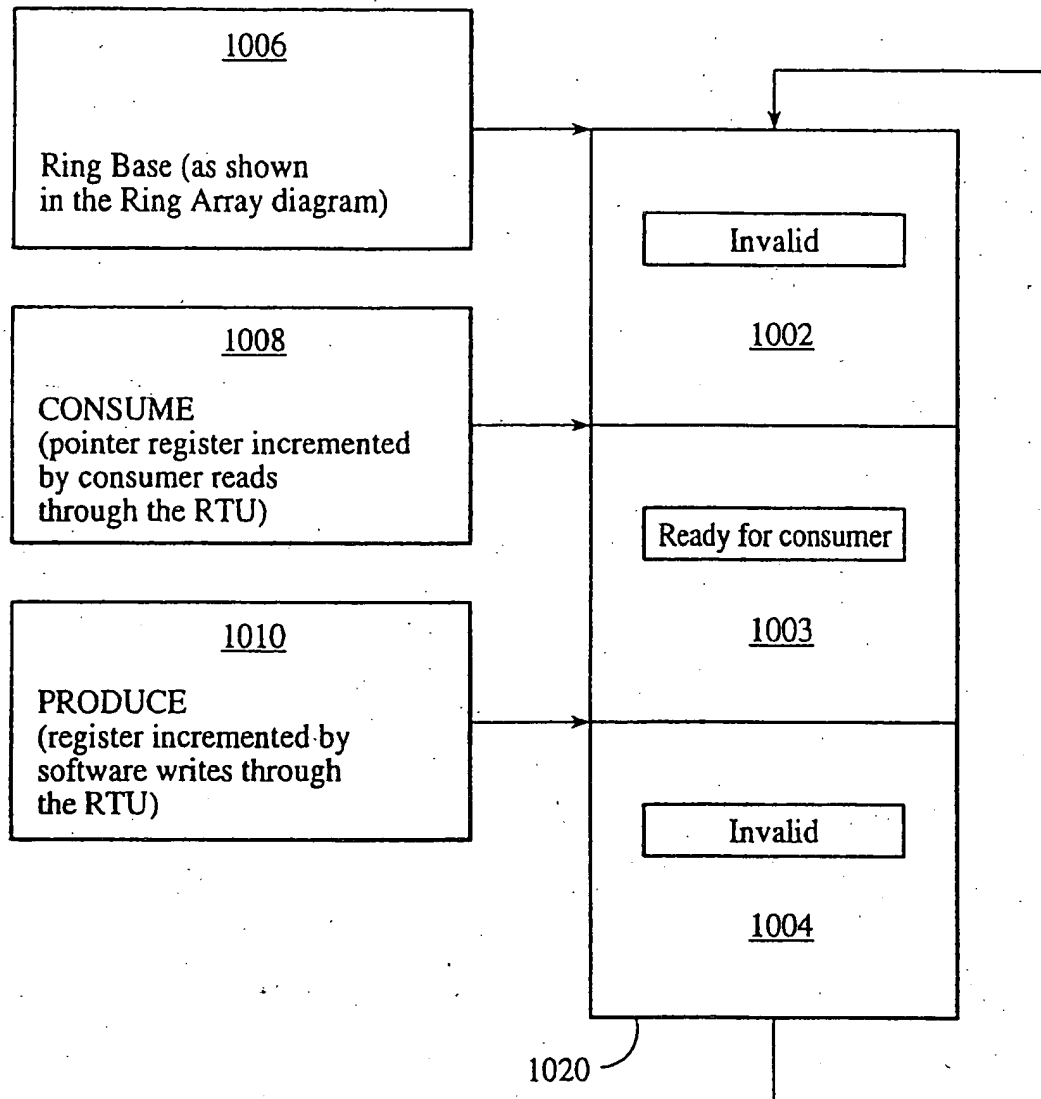


FIG. 11

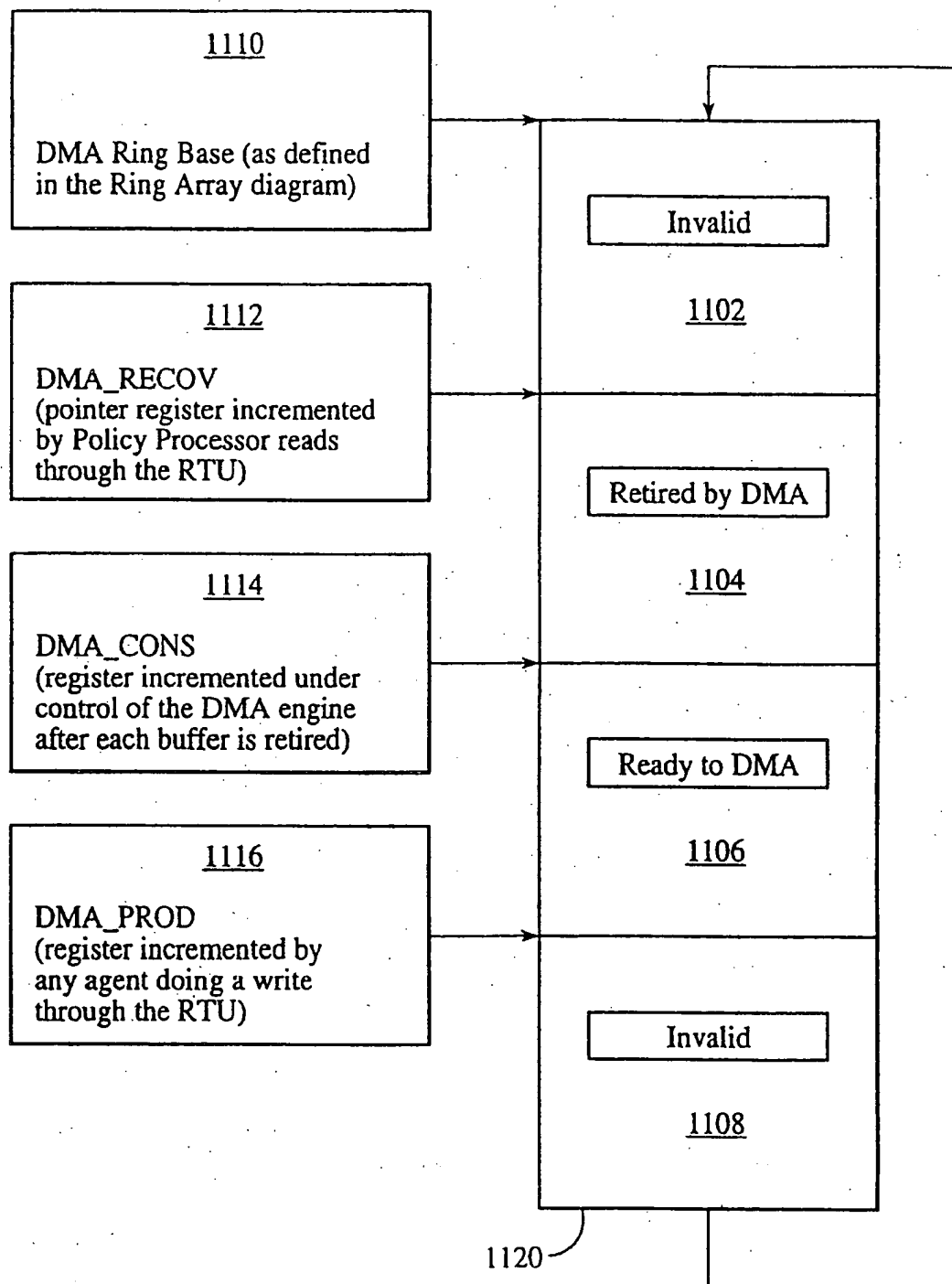
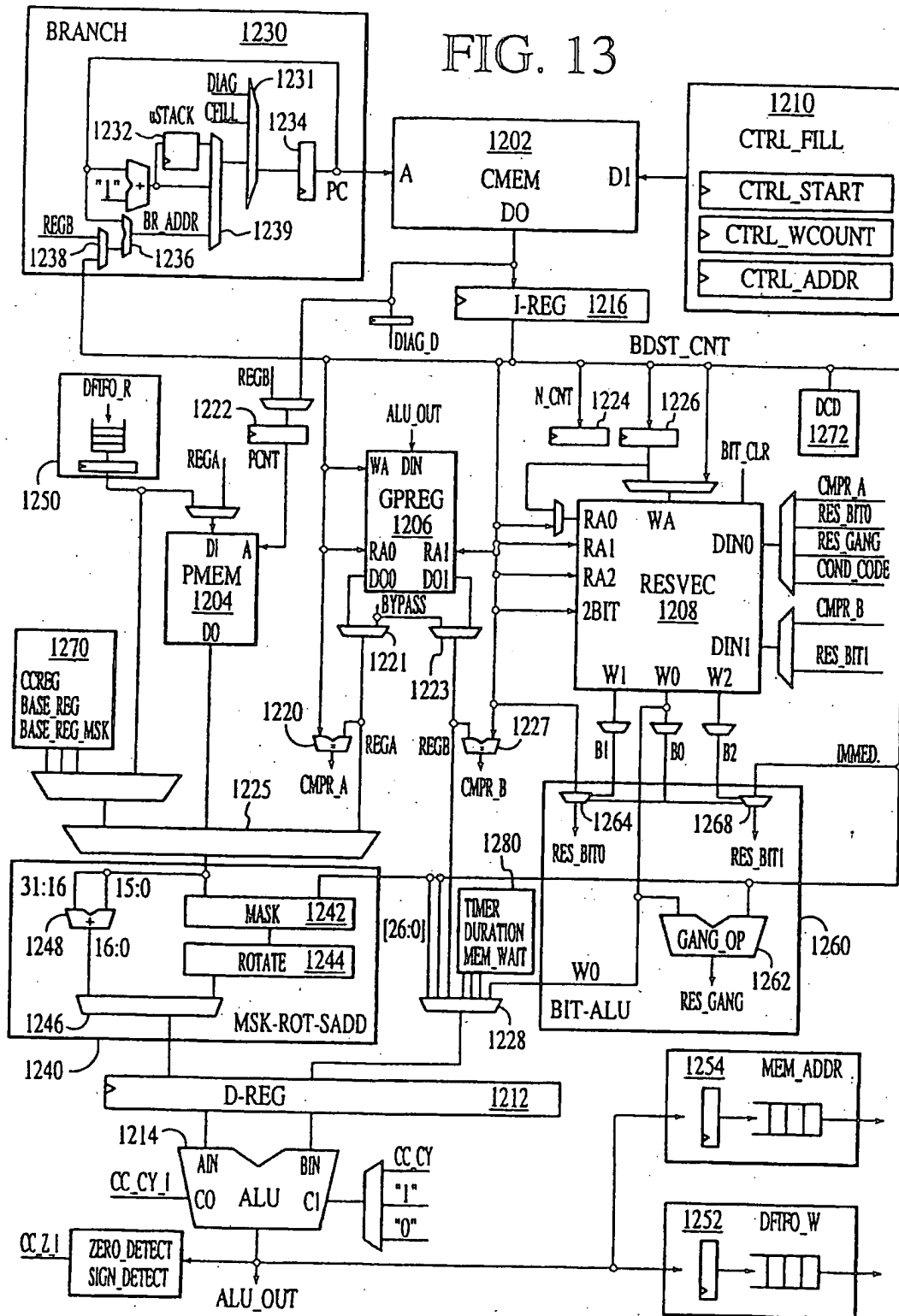


FIG. 12

FIG. 13



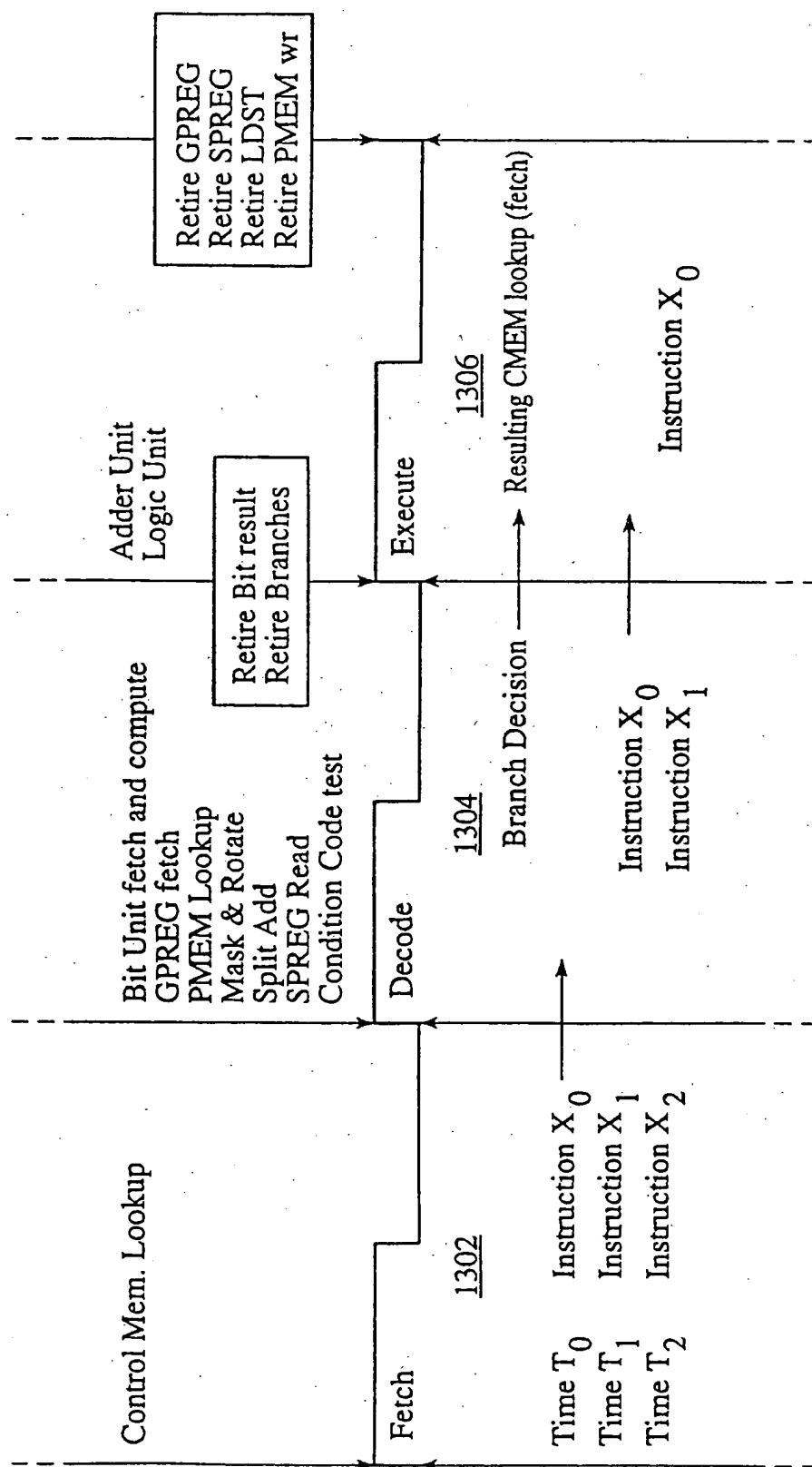


FIG. 14

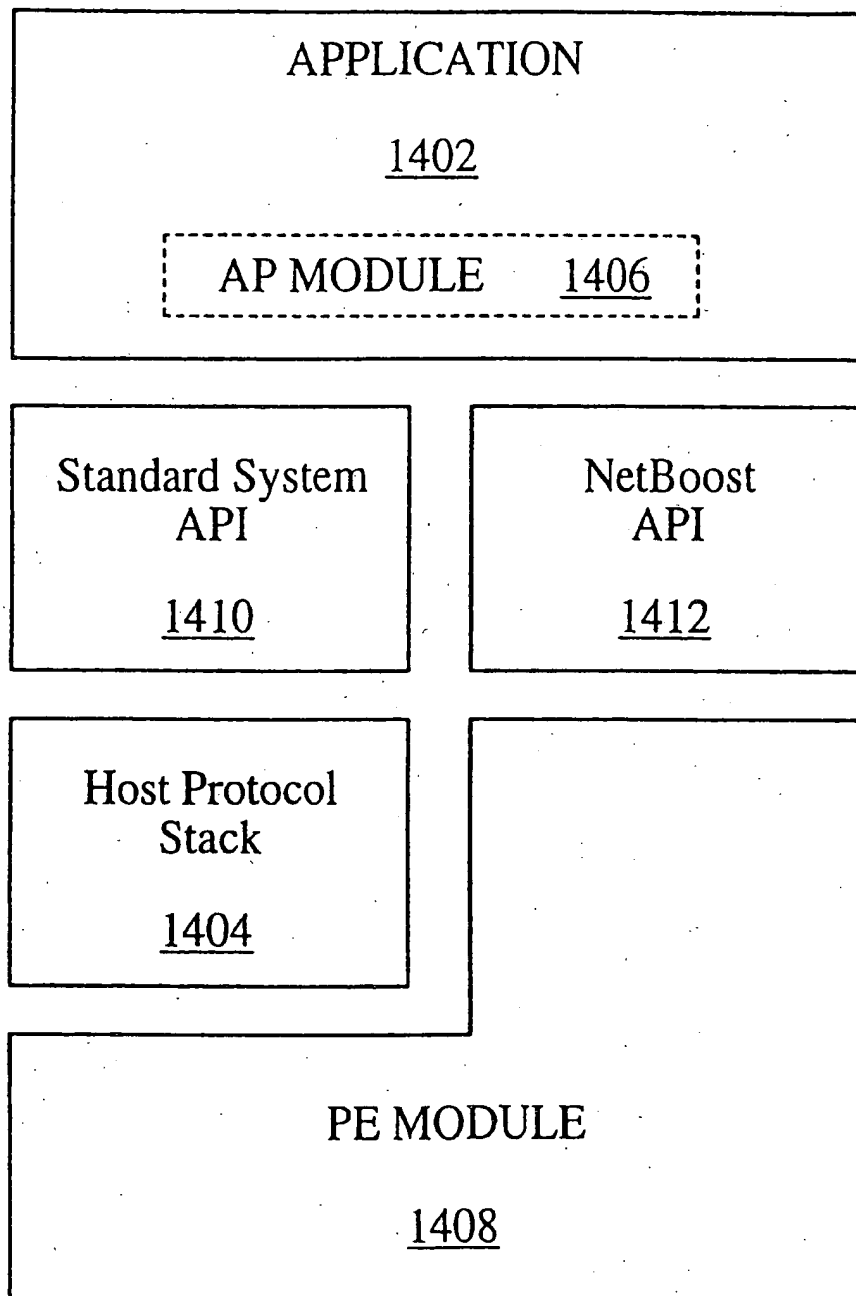


FIG. 15

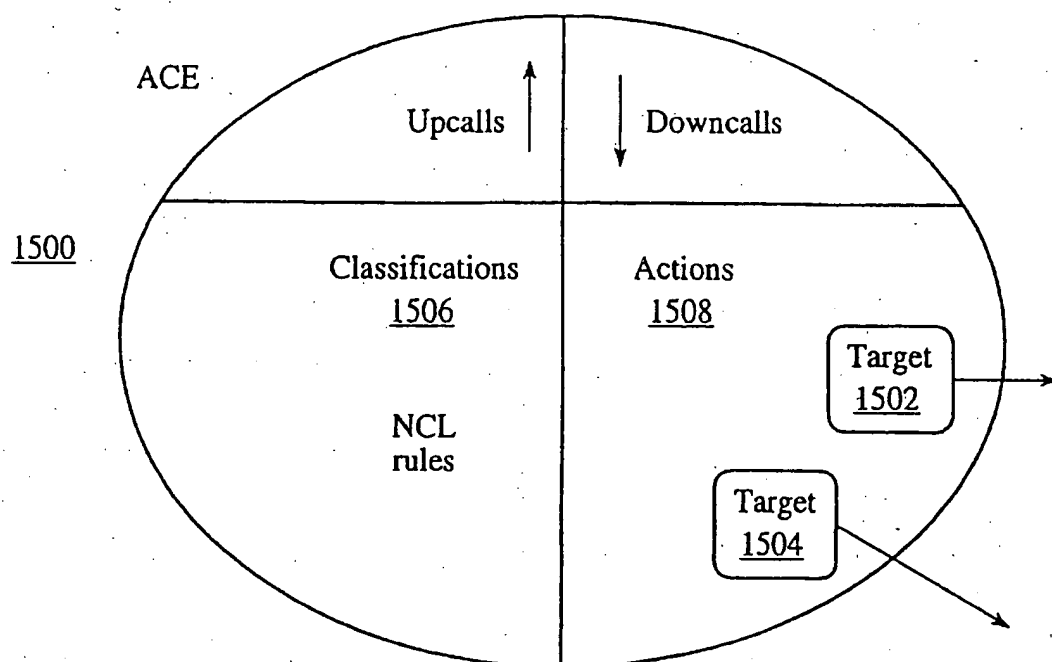


FIG. 16

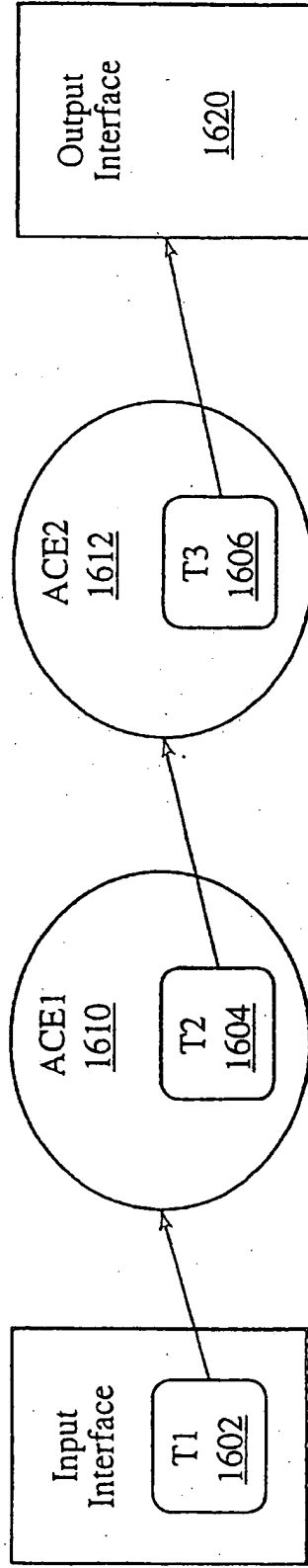


FIG. 17

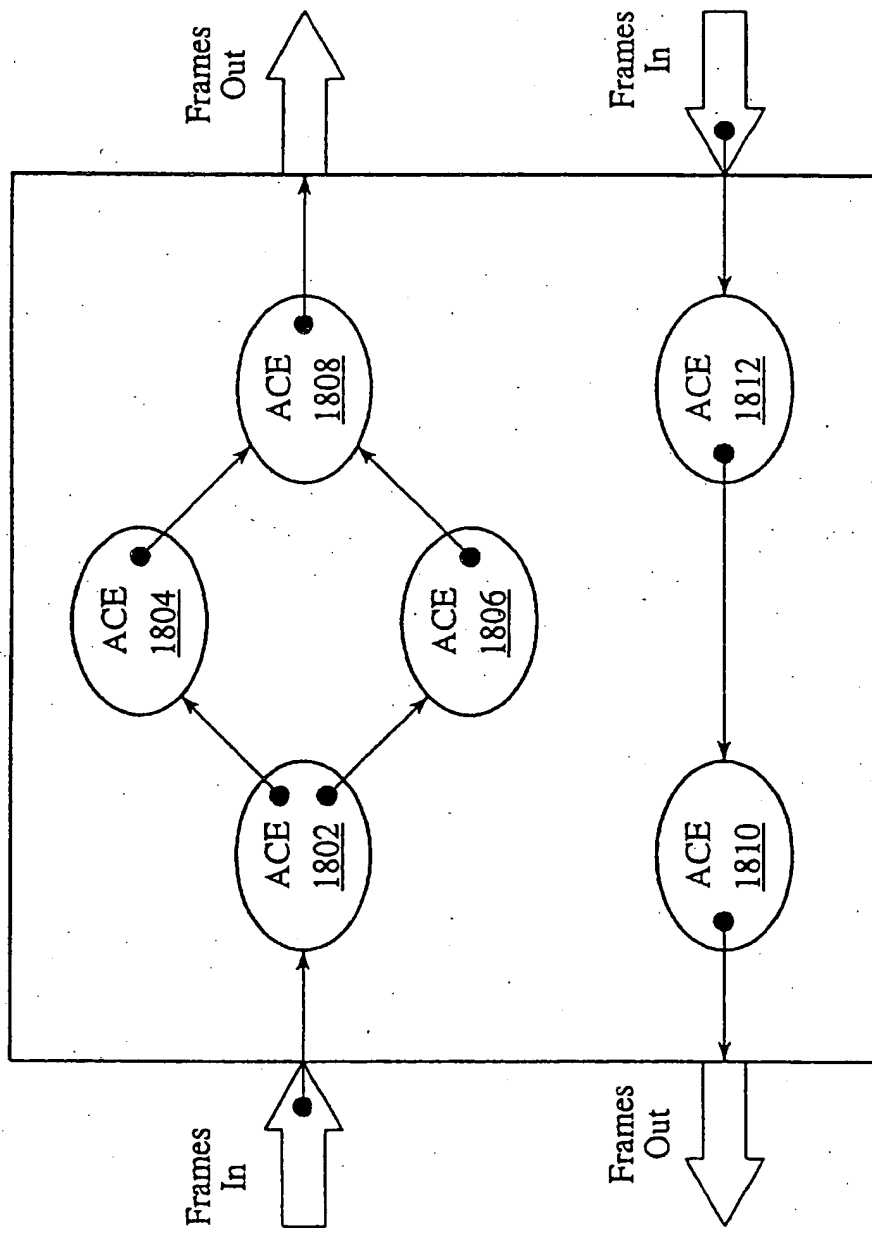


FIG. 18

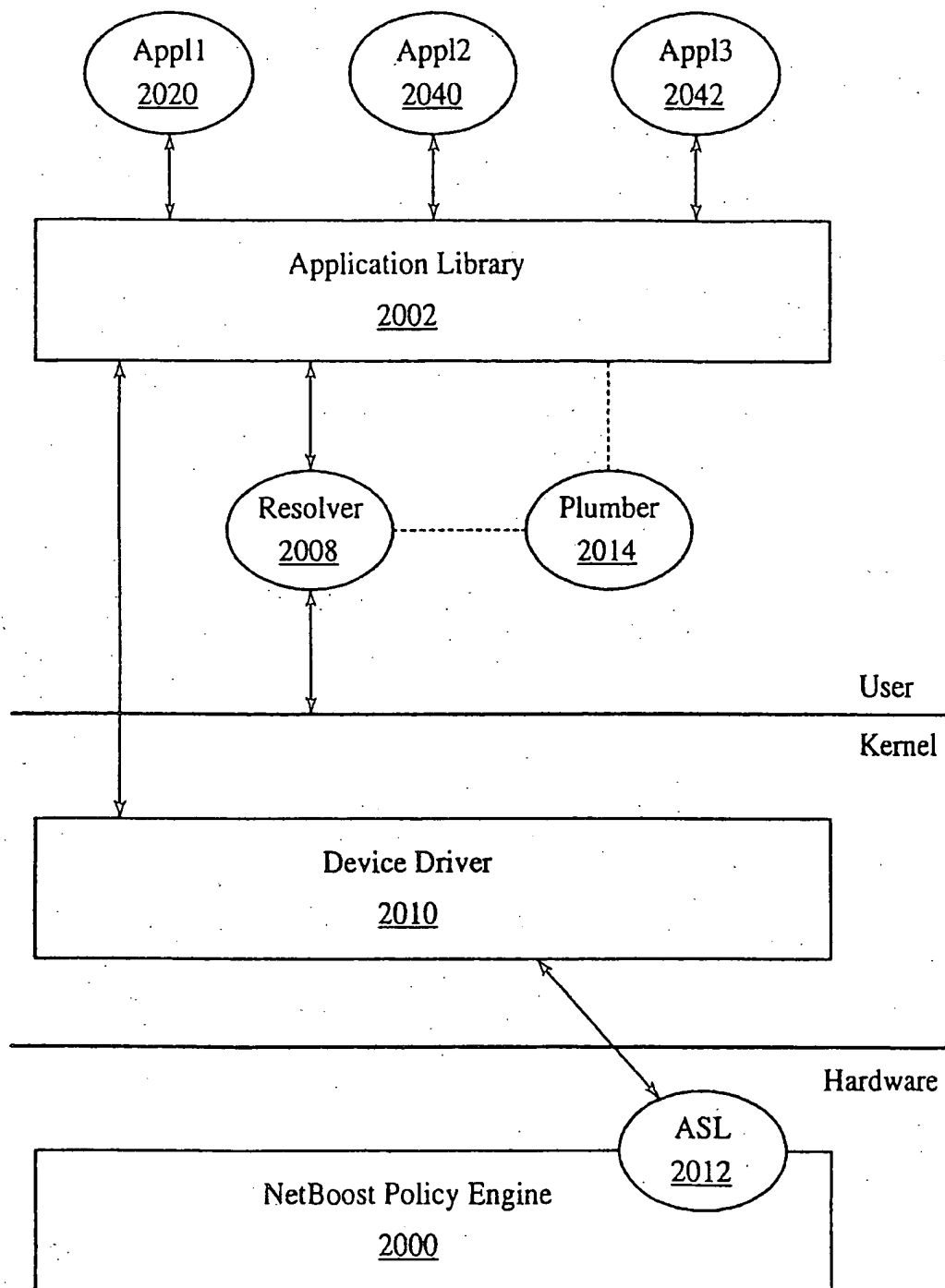


FIG. 19